EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	2	2003/0002596 2003/0002607 2003/0122588 2003/0123589 2003/0123594	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:13
S1	1	"20050251710"	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:13
S3	5	"20030002596" "20030002607" "20030122588" "20030123589" "20030123594"	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:16
S4	3	10/383501 10/400957	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:18
S5	17	(Tarango ADJ Tony Swartz ADJ Ronald).in.	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:19
S8	3	(Tarango ADJ Tony Swartz ADJ Ronald).in. and phase with loop	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:20
S6	20	(Tarango ADJ Tony Swartz ADJ Ronald).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:20
S7	6	(Tarango ADJ Tony Swartz ADJ Ronald).in. and phase	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:21
S10	21	phase with loop and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:25
S11	5	phase with loop and jitter$3 and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:36
S13	1	phase near lock$3 near loop and jitter$3 and offset and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:39
S14	9851	phase near lock$3 near loop and jitter$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:43
S15	9828	phase adj lock$3 adj loop and jitter$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:44

S18	231	"714"/$.ccls. and phase adj lock$3 adj loop and jitter$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:46
S16	2094	phase adj lock$3 adj loop with jitter$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:46
S19	16	"714"/$.ccls. and phase adj lock$3 adj loop with jitter$3 and offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:50
S23	14	phase adj lock$3 adj loop with jitter$3 with data adj recover$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:58
S17	48	"714"/$.ccls. and phase adj lock$3 adj loop with jitter$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:58
S28	14	phase with loop with recovery with jitter$3 and link and integrated adj circuit and transmi$5 with receiv$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:15
S27	34	phase with loop with recovery with jitter$3 and integrated adj circuit and transmi$5 with receiv$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:15
S30	70478	phase adj lock$3 loop with recovery with jitter$3 and link and integrated adj circuit and transmi$5 with receiv$3 and @ad<"20040330"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:17
S29	13	phase with loop with recovery with jitter$3 and link and integrated adj circuit and transmi$5 with receiv$3 and @ad<"20040330"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:17
S31	11	phase adj lock$3 adj loop with recovery with jitter$3 and link and integrated adj circuit and transmi$5 with receiv$3 and @ad<"20040330"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:18
S22	6	"714"/$.ccls. and phase adj lock$3 adj loop and jitter$3 near3 offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:21

S32	5	"714"/$.ccls. and phase adj lock$3 adj loop and jitter$3 near3 simulat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:25
S33	56	phase adj lock$3 adj loop and jitter$3 near3 simulat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:26
S35	1	("6671863").PN.	USPAT	OR	OFF	2006/03/30 15:29
S34	4	phase adj lock$3 adj loop with jitter$3 near3 simulat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:30
S36	56	phase adj lock$3 adj loop and jitter$3 near3 simulat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:36
S38	1	phase adj lock$3 adj loop with jitter$3 near3 phase adj offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:37
S37	34	phase adj lock$3 adj loop and jitter$3 near3 phase adj offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:37
S40	2	"20050135501"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:42
S39	4	phase adj lock$3 adj loop same jitter$3 near3 phase adj offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:42
S41	2	("3737766" | "5900755").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 15:47
S12	5	phase near lock$3 near loop and jitter$3 and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:57
S43	3897	phase adj lock$3 adj loop and jitter$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:59
S45	655	phase adj lock$3 adj loop and jitter$3 and data adj recover$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:00

S44	3	phase adj lock$3 adj loop and jitter$3 and data adj recover$3 and integrated adj circuit with under adj test	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:00
S46	89	phase adj lock$3 adj loop with jitter$3 and data adj recover$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:01
S47	25	phase adj lock$3 adj loop with jitter$3 and phase adj (offset adjust$5 var$6) and data adj recover$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:02
S50	1	"6331999".PN.	USPAT; USOCR	OR	OFF	2006/03/30 17:34
S49	1	"4809306".PN.	USPAT; USOCR	OR	OFF	2006/03/30 17:34
S51	1	"324"/$.ccls. and phase adj lock$3 adj loop with jitter$3 and phase adj (offset adjust$5 var$6) and data adj recover$3	USPAT	OR	ON	2006/03/30 17:43
S48	12	phase adj lock$3 adj loop with jitter$3 and phase adj (offset adjust$5 var$6) and data adj recover$3 and integrated adj circuit	USPAT	OR	ON	2006/03/30 17:43
S52	8	"324"/$.ccls. and phase adj lock$3 adj loop with jitter$3 and phase adj (offset adjust$5 var$6)	USPAT	OR	ON	2006/03/30 17:50
S53	3	"324"/$.ccls. and phase adj lock$3 adj loop and jitter$3 with phase adj (offset adjust$5 var$6)	USPAT	OR	ON	2006/03/30 17:57
S54	5	"324"/$.ccls. and phase adj lock$3 adj loop and jitter$3 same phase adj (offset adjust$5 var$6)	USPAT	OR	ON	2006/03/30 17:58
S56	4	"324"/$.ccls. and phase adj lock$3 adj loop and jitter$3 and phase adj (offset adjust$5 var$6) and "IC"	USPAT	OR	ON	2006/03/30 18:01
S55	28	"324"/$.ccls. and phase adj lock$3 adj loop and jitter$3 and phase adj (offset adjust$5 var$6)	USPAT	OR	ON	2006/03/30 18:01
S57	57	phase with loop with recovery with jitter$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/03 14:54